

Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset

TITLE	PAGE
COVER SHEET	1
BLOCK DIAGRAM	2
CPU	3,4
CK-410 CLOCK SYNTHESIZER	5
INTEL® 915GM GMCH	6,7,8,9
DDR2 SO-DIMM	10,11
INTEL® 82801FBM ICH6-M ICH	12,13,14
IDE,CF,USB,FP	15
LVDS,SATA,FWH,PS/2	16
LAN(INTEL® 82551QM/82541ER)	17,18
SUPER I/O(W82627HF),FDD,DIO	19
COM PORT	20
VGA,TV-OUT	21
EXTENSION SLOT	22
AC-97 CODEC	23
PRT,RESET,H/W MONITOR	24
DC DC IMVP-IV SC451	25
DC DC GMCH,DDR2	26
DC POWER IN	27
POWER DIAGRAM	28

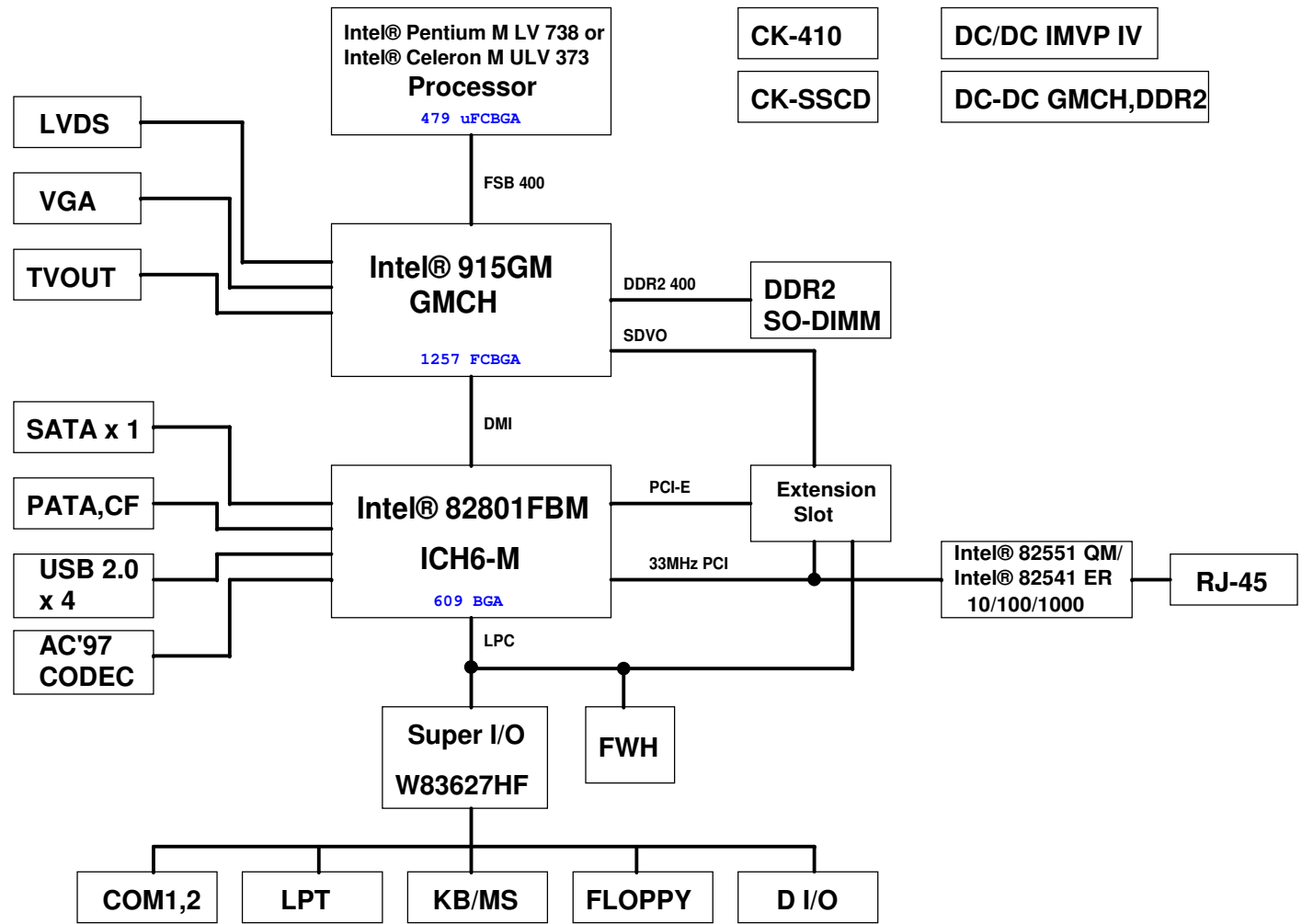
ON BOARD PCI LAN/AD20/INT-D/REQ0
PCI1/AD17/INT-FGHD/REQ1
PCI2/AD18/INT-GHDF/REQ2

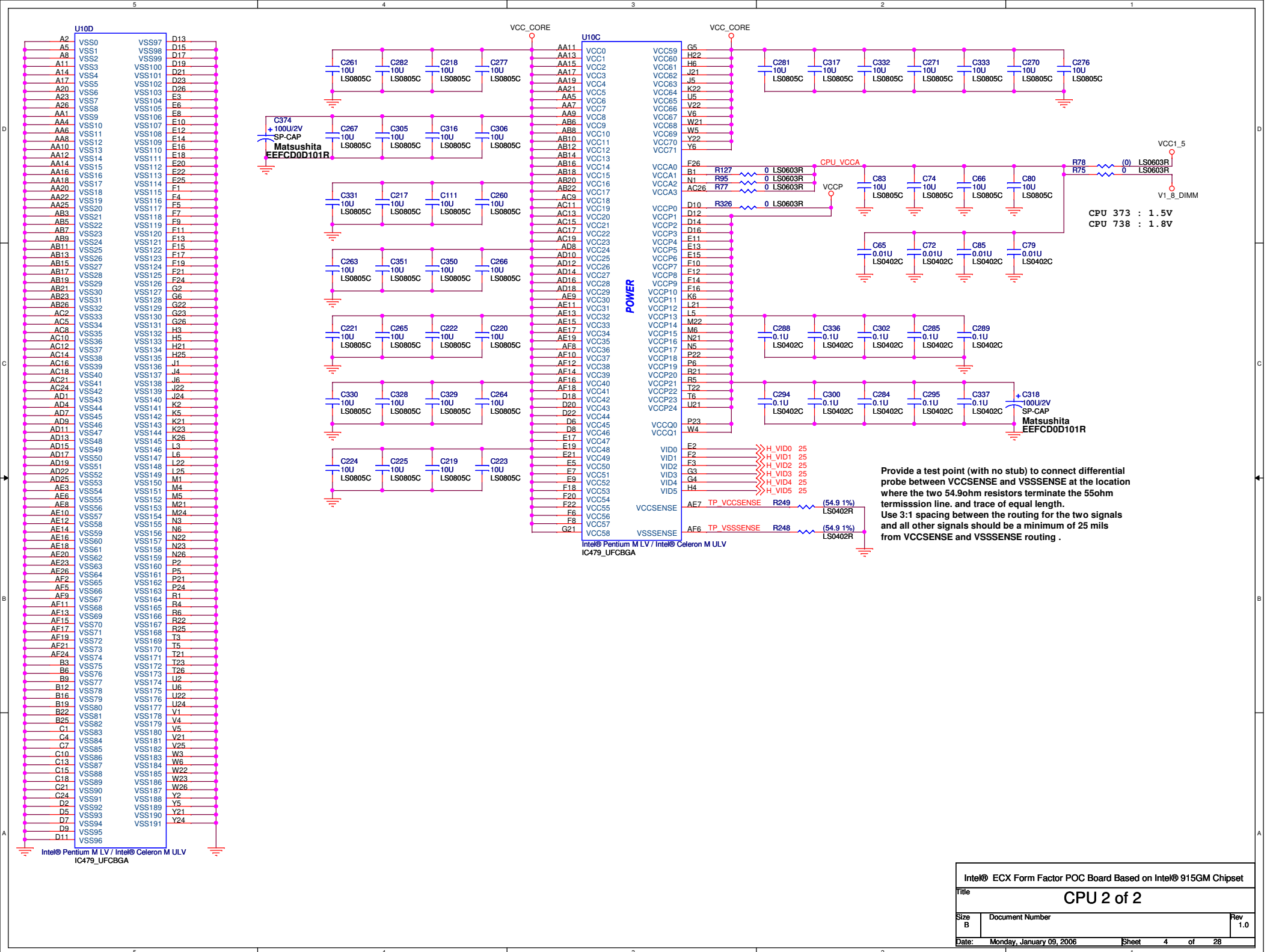
Disclaimer

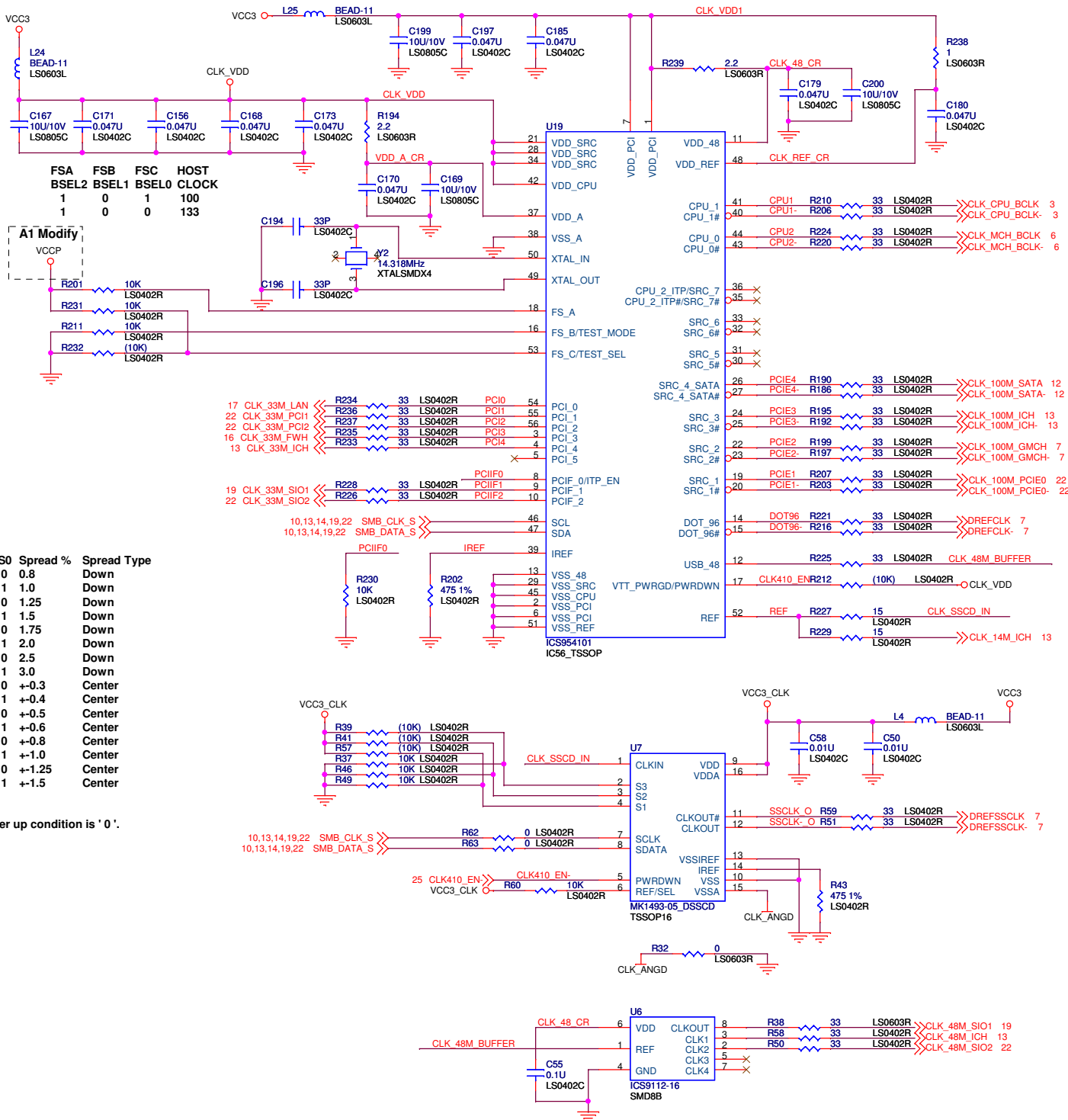
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Title Cover Sheet		
Size B	Document Number	Rev 1.0
Date: Monday, January 09, 2006	Sheet 1	of 28



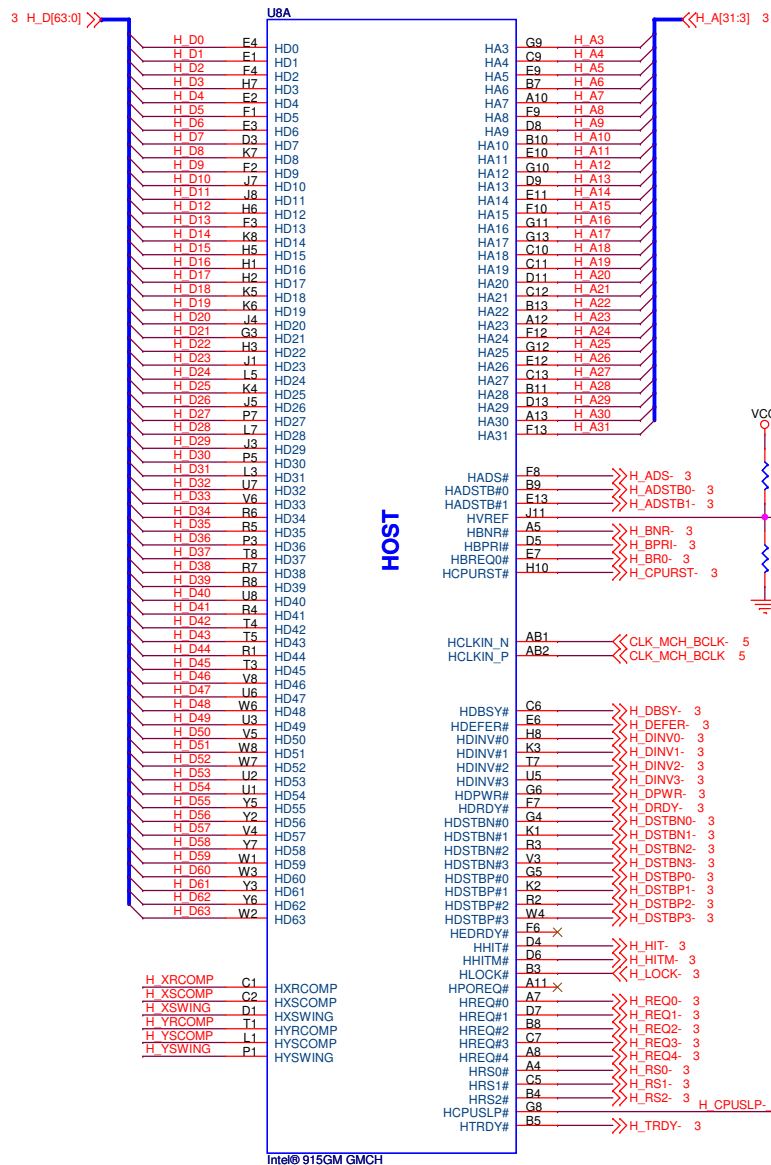
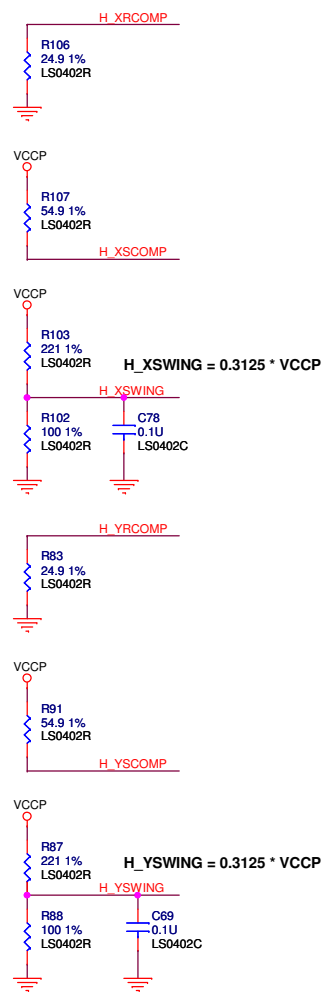


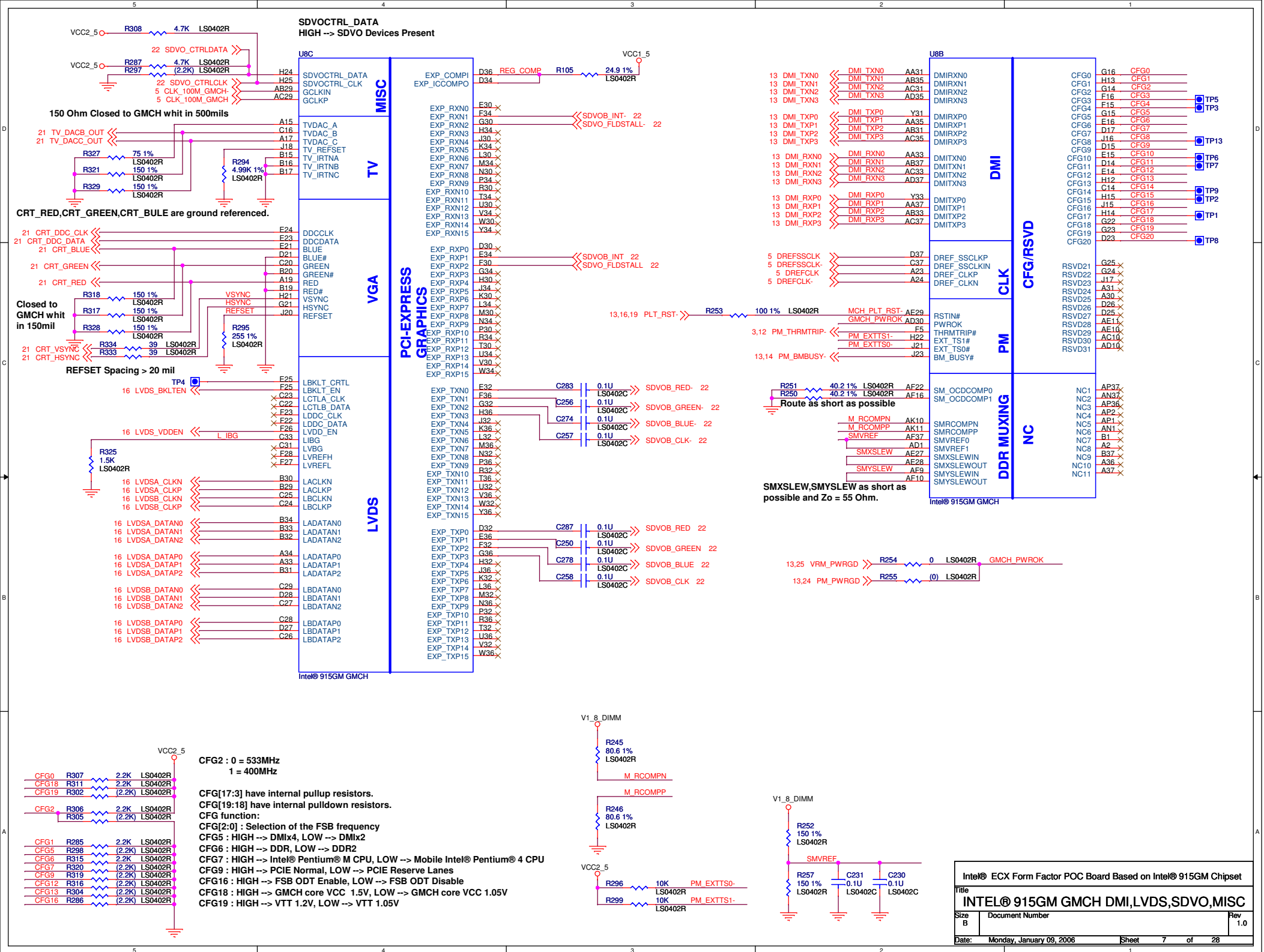


CLK_MCH_BCLK	R223	49.9 1%	LS0402R
CLK_MCH_BCLK-	R219	49.9 1%	LS0402R
CLK_CPU_BCLK	R209	49.9 1%	LS0402R
CLK_CPU_BCLK-	R205	49.9 1%	LS0402R
CLK_100M_GMCH	R200	49.9 1%	LS0402R
CLK_100M_GMCH-	R198	49.9 1%	LS0402R
CLK_100M_SATA	R191	49.9 1%	LS0402R
CLK_100M_SATA-	R187	49.9 1%	LS0402R
CLK_100M_ICH	R196	49.9 1%	LS0402R
CLK_100M_ICH-	R193	49.9 1%	LS0402R
CLK_100M_PCIE0	R208	49.9 1%	LS0402R
CLK_100M_PCIE0-	R204	49.9 1%	LS0402R
DREFCLK	R222	49.9 1%	LS0402R
DREFCLK-	R217	49.9 1%	LS0402R
DREFSSCLK	R61	49.9 1%	LS0402R
DREFSSCLK-	R48	49.9 1%	LS0402R
CLK_14M_ICH	C184	10P	LS0402C
CLK_SSCD_IN	C182	10P	LS0402C
CLK_48M_ICH	C57	10P	LS0402C
CLK_48M_SIO1	C51	10P	LS0402C
CLK_48M_SIO2	C53	10P	LS0402C
CLK_33M_ICH	C189	10P	LS0402C
CLK_33M_SIO1	C183	10P	LS0402C
CLK_33M_SIO2	C181	10P	LS0402C
CLK_33M_FWH	C191	10P	LS0402C
CLK_33M_PCI1	C192	10P	LS0402C
CLK_33M_PCI2	C198	10P	LS0402C
CLK_33M_LAN	C190	10P	LS0402C

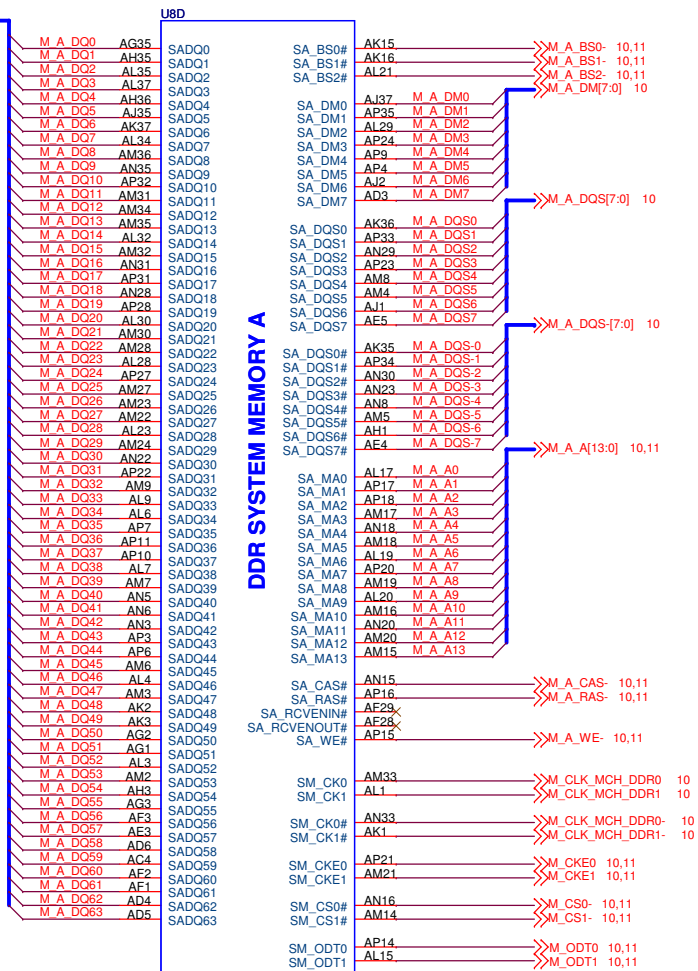
S3	S2	S1	S0	Spread %	Spread Type
0	0	0	0	0.8	Down
0	0	0	1	1.0	Down
0	0	1	0	1.25	Down
0	0	1	1	1.5	Down
0	1	0	0	1.75	Down
0	1	0	1	2.0	Down
0	1	1	0	2.5	Down
0	1	1	1	3.0	Down
1	0	0	0	+0.3	Center
1	0	0	1	+0.4	Center
1	0	1	0	+0.5	Center
1	0	1	1	+0.6	Center
1	1	0	0	+0.8	Center
1	1	0	1	+1.0	Center
1	1	1	0	+1.25	Center
1	1	1	1	+1.5	Center

The S0 power up condition is '0'.

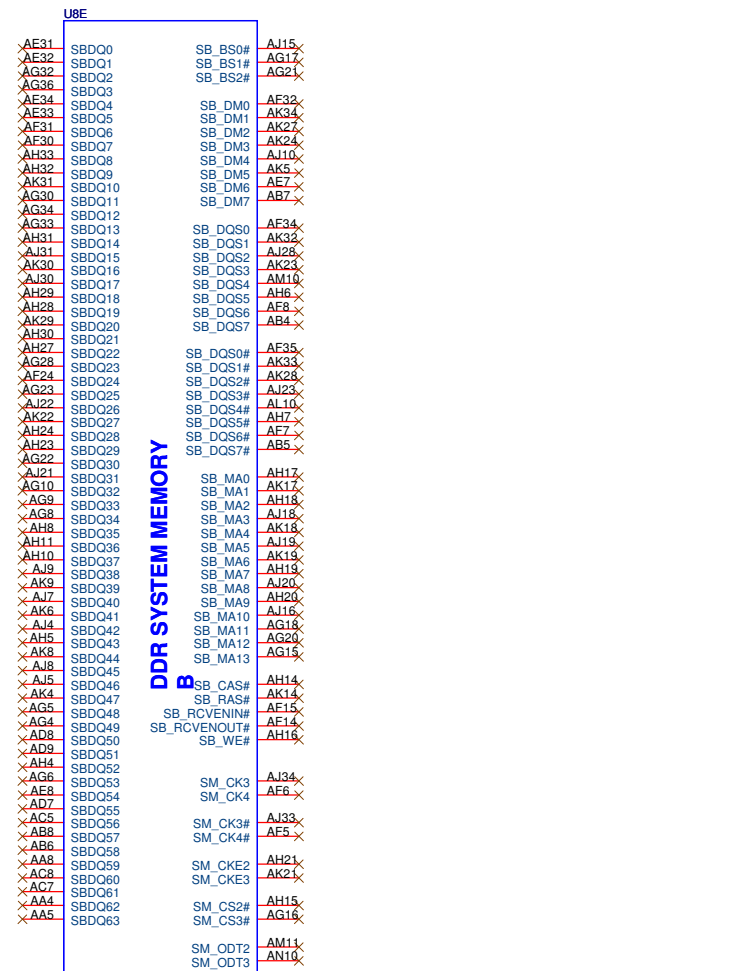




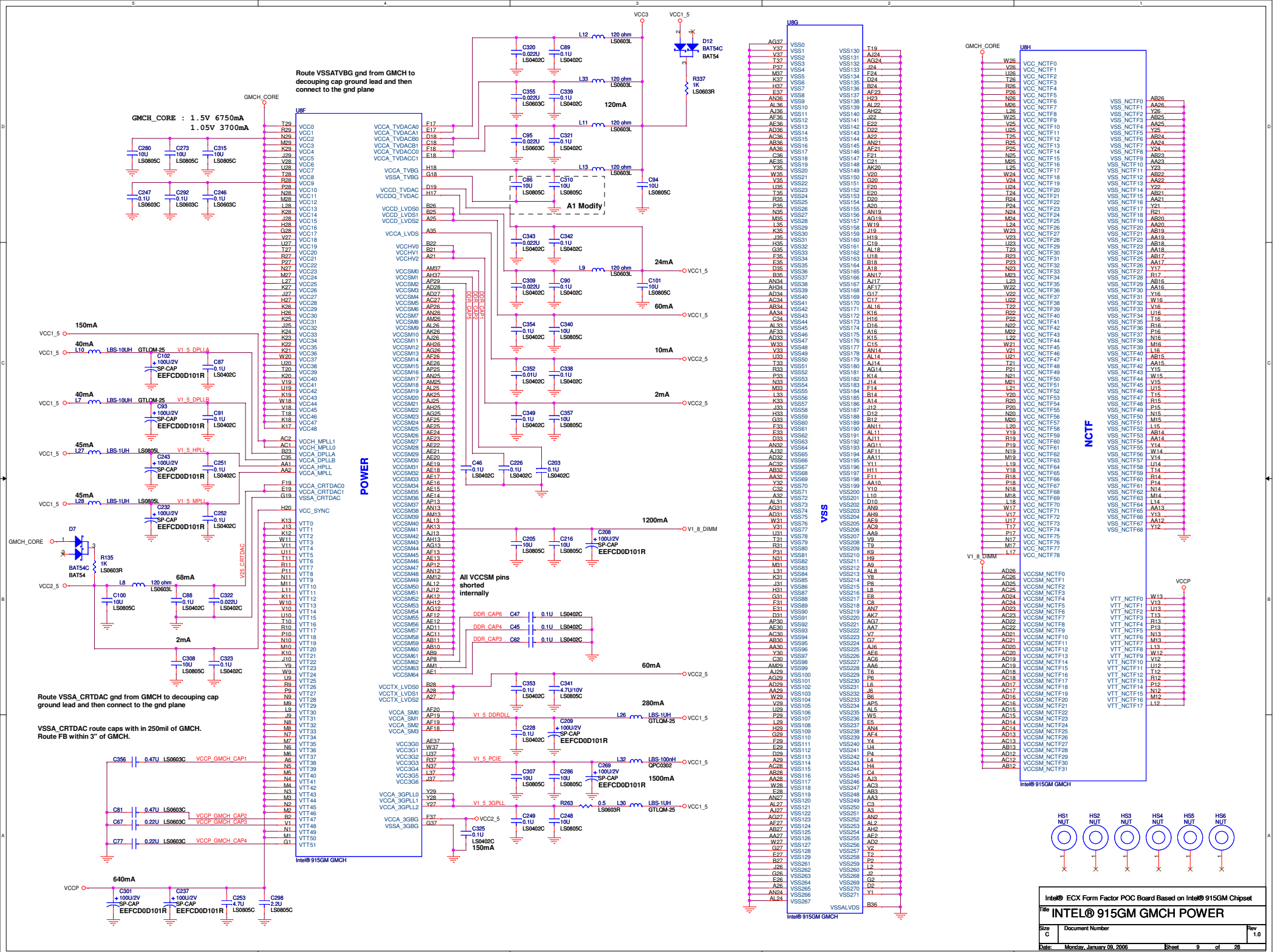
10 M_A_DQ[63:0] <<



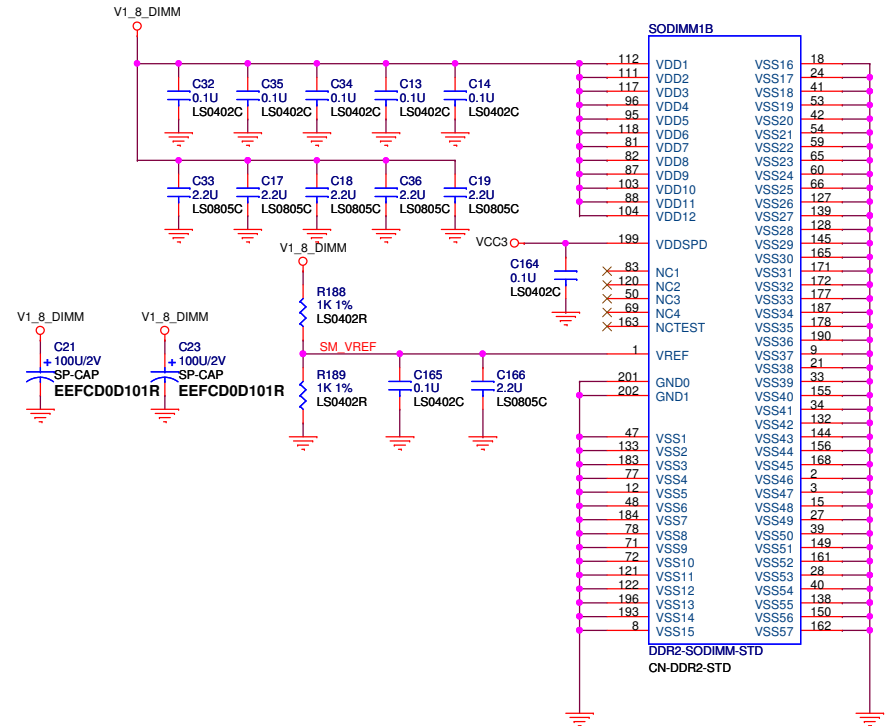
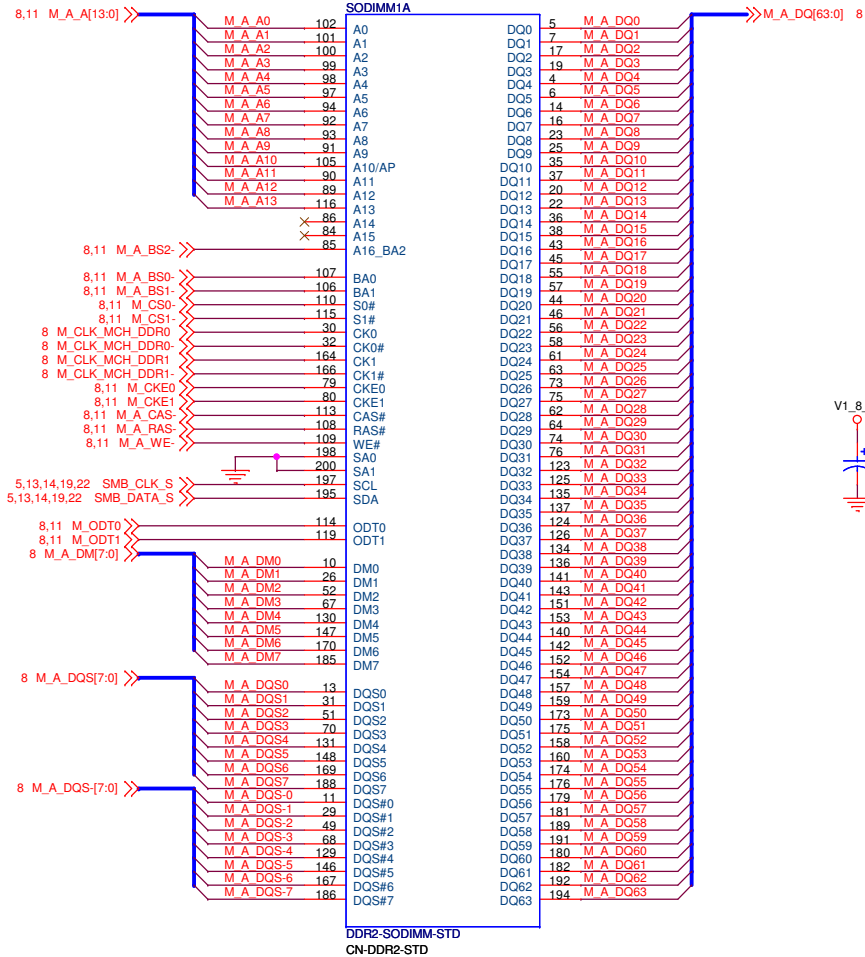
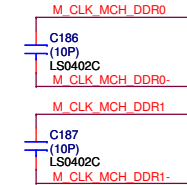
Intel® 915GM GMCH



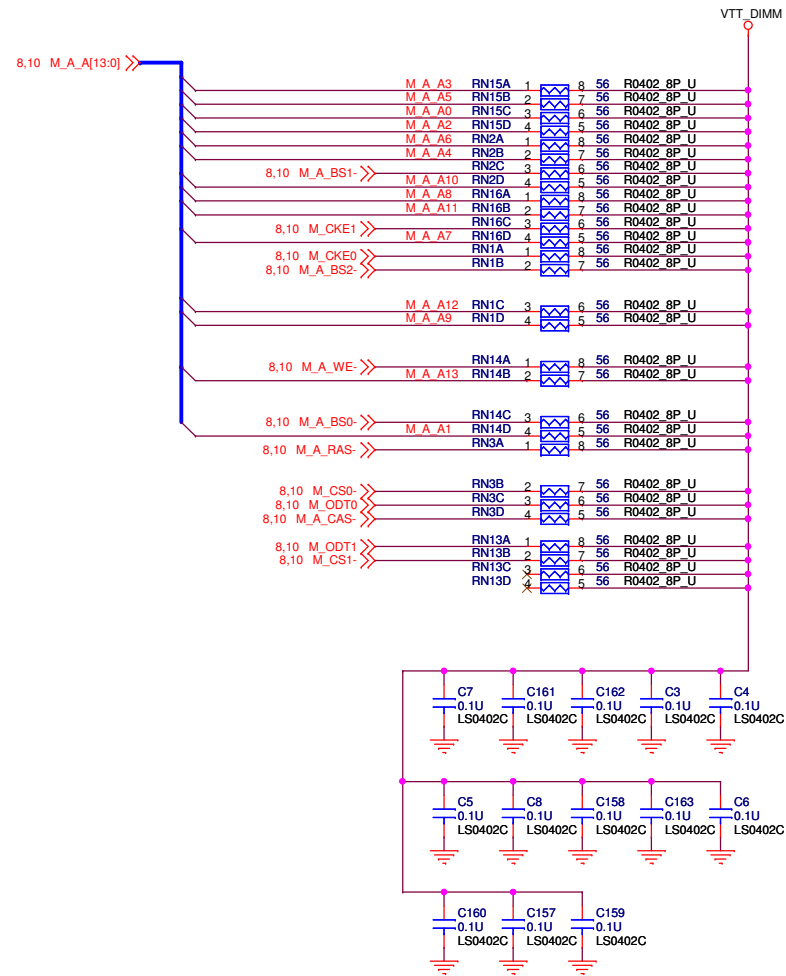
Intel® 915GM GMCH



PLACE NEAR DDR2 SO-DIMM

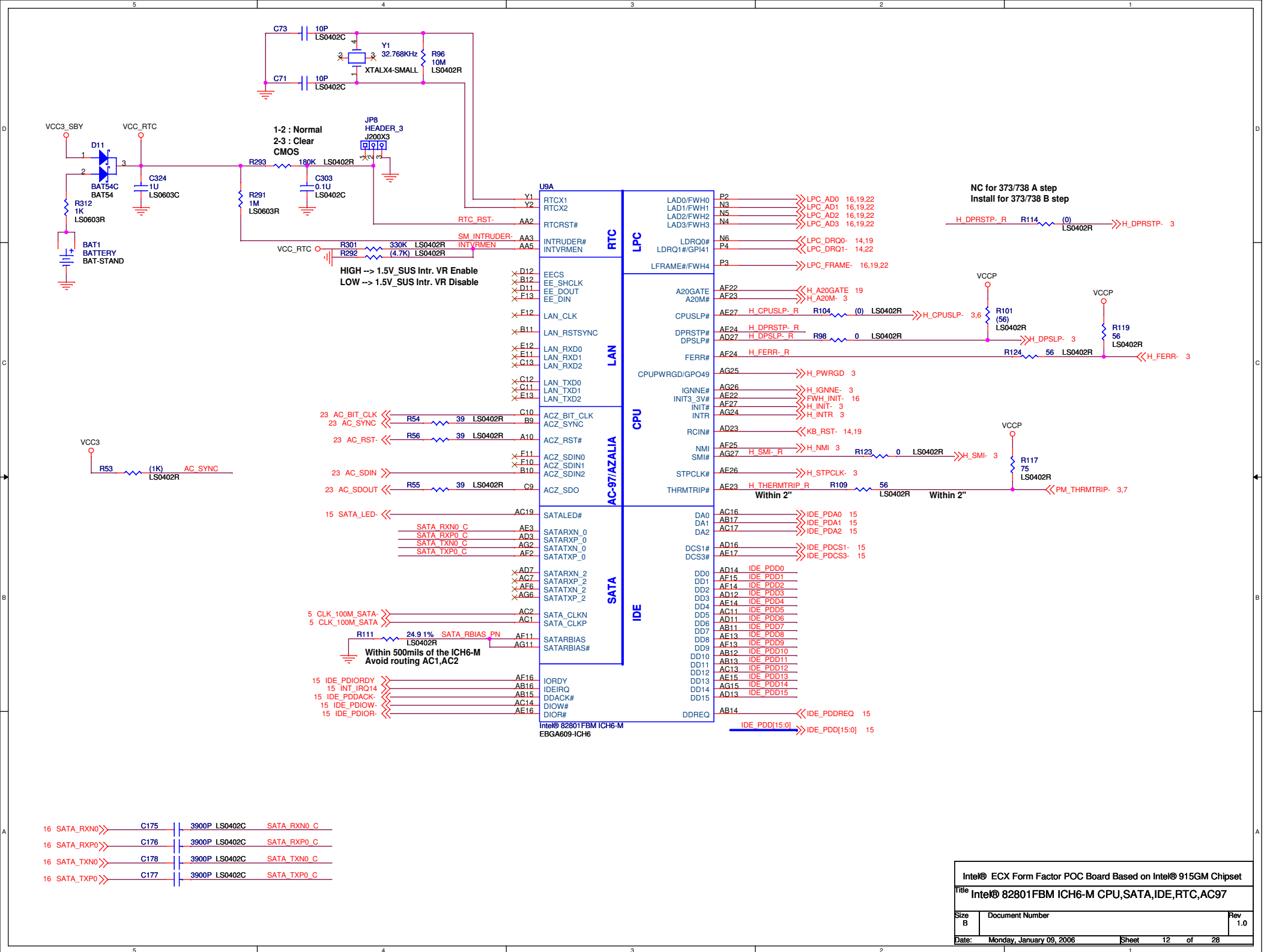


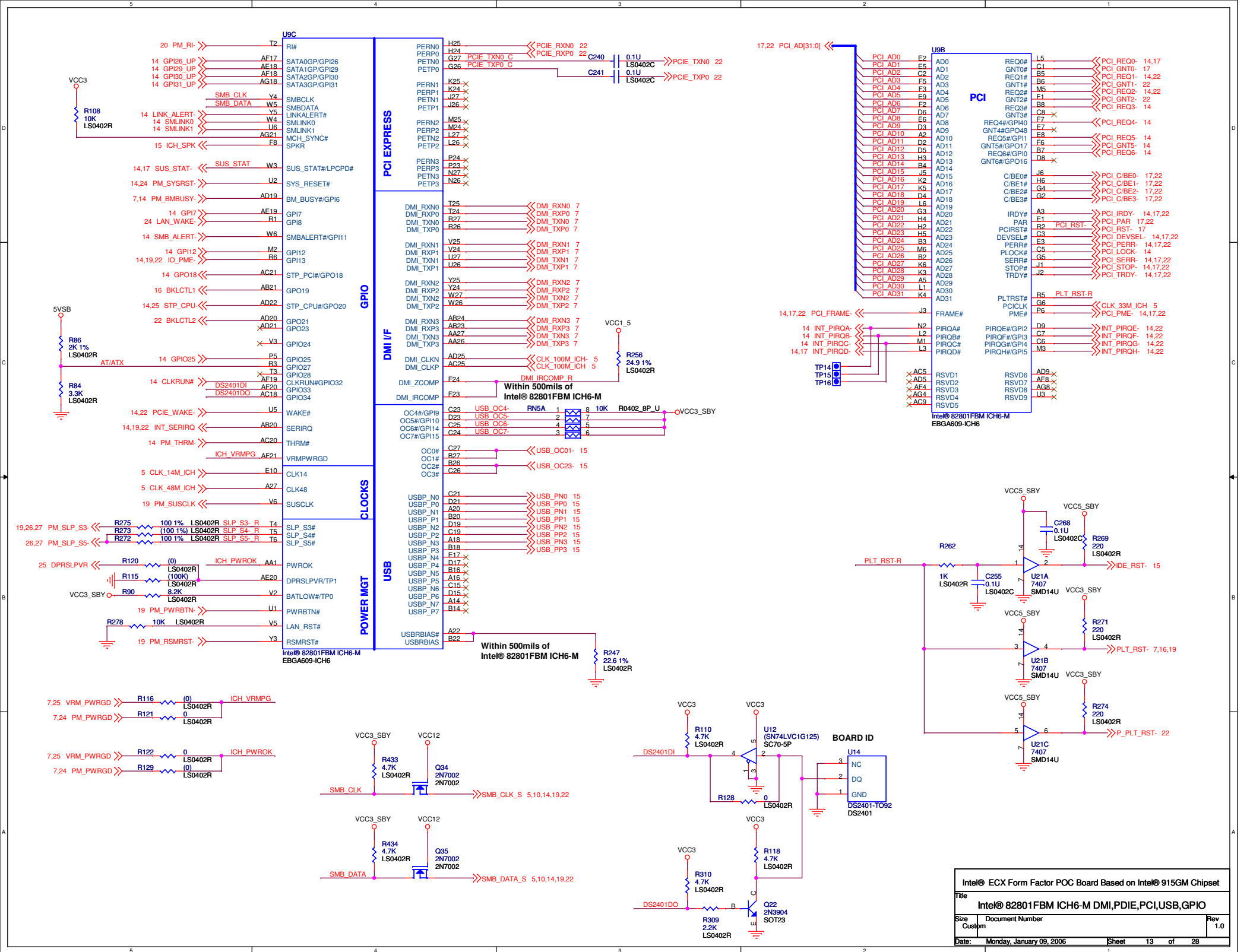
Parallel Termination



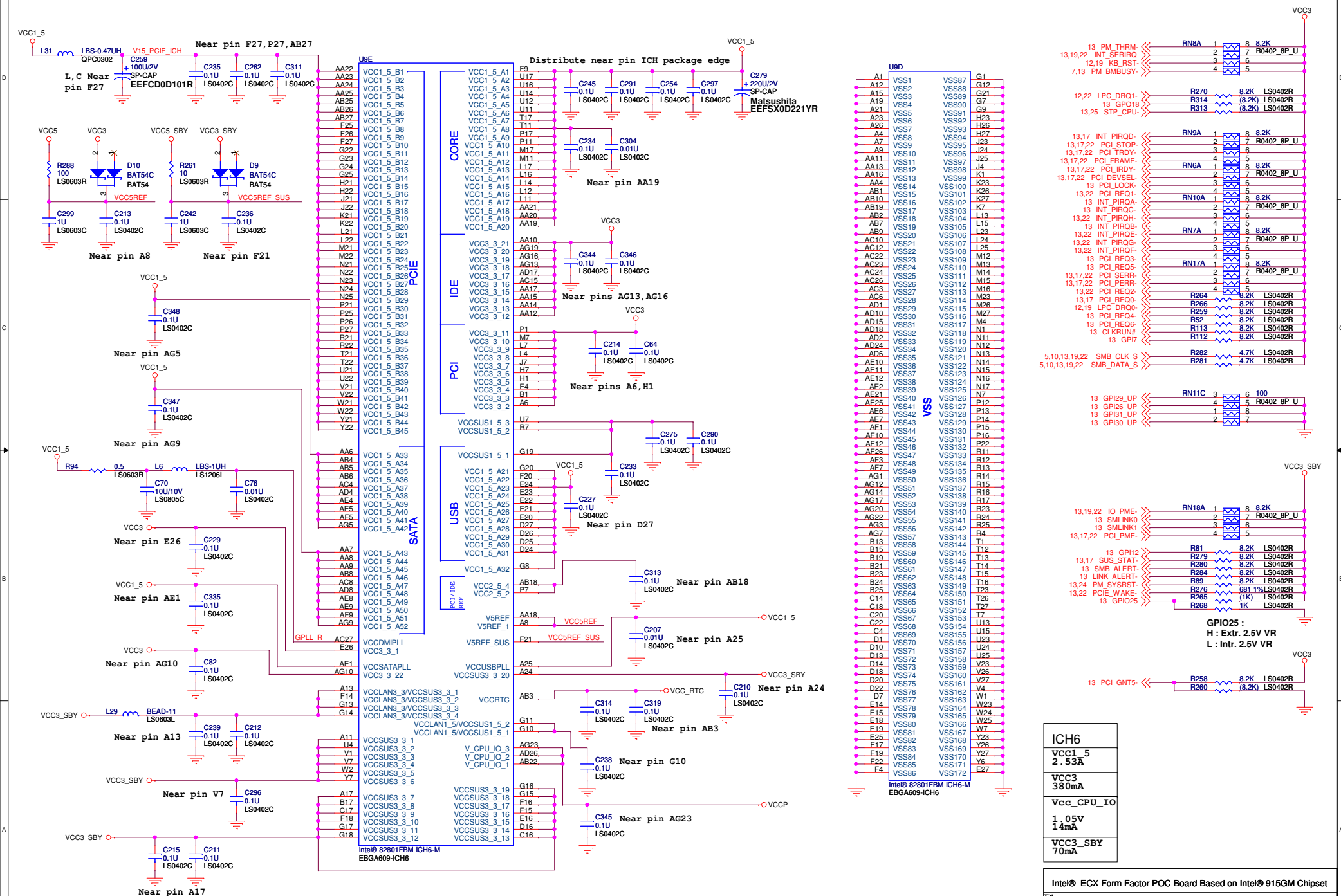
PLACE ONE CAP CLOSE TO EVERY 2 PULLUP RESISTORS TERMINATED TO VTT_DIMM

Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset			
Title		DDR2 TERMINATION	
Size B	Document Number		Rev 1.0
Date:	Monday, January 09, 2006	Sheet 11 of 26	





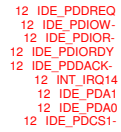
All Capacitors should be placed less than 100 mils from the package



ICH6
VCC1_5 2.53A
VCC3 380mA
Vcc_CPU_IC 1.05V 14mA
VCC3_SBY 70mA

Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset			
Title Intel® 82801FBM ICH6-M POWER			
Size Custom	Document Number		Rev 1.0
Date: Monday, January 09, 2006	Sheet	14 of 28	

12 IDE_PDD[15:0] << IDE_PDD[15:0]



DEFAULT 1-2 Short



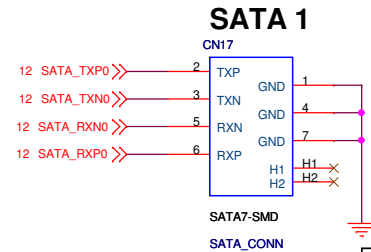
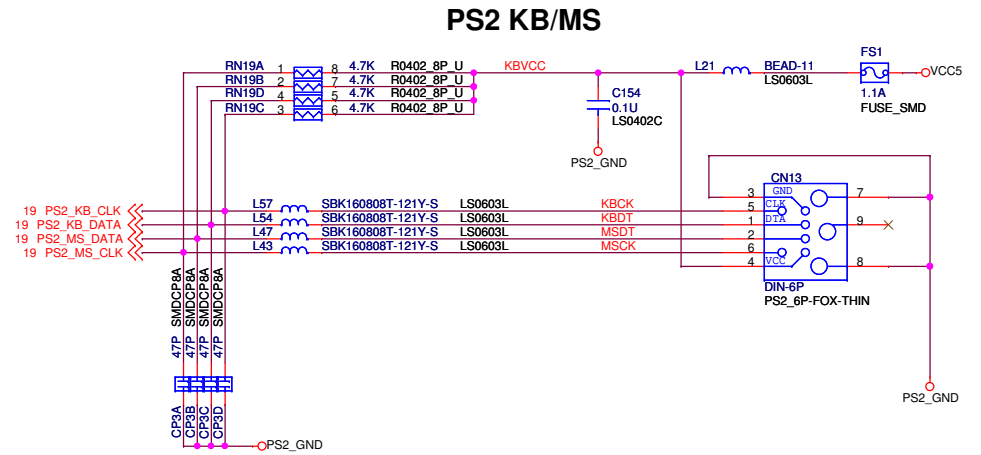
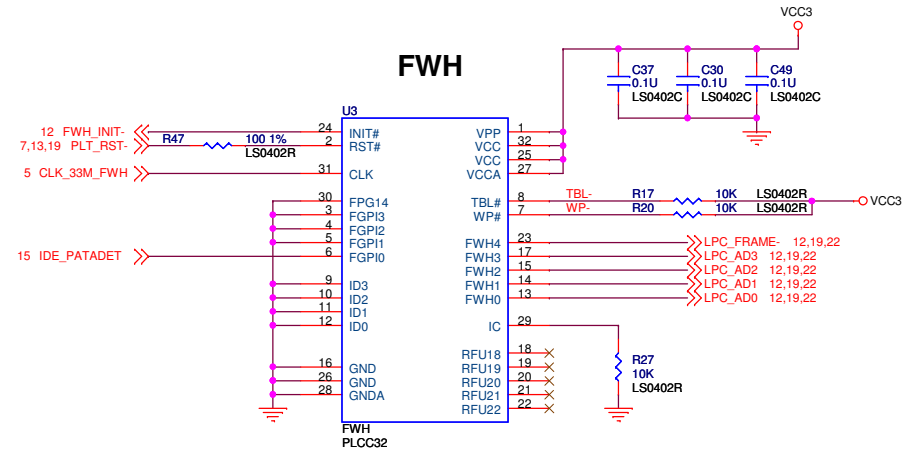
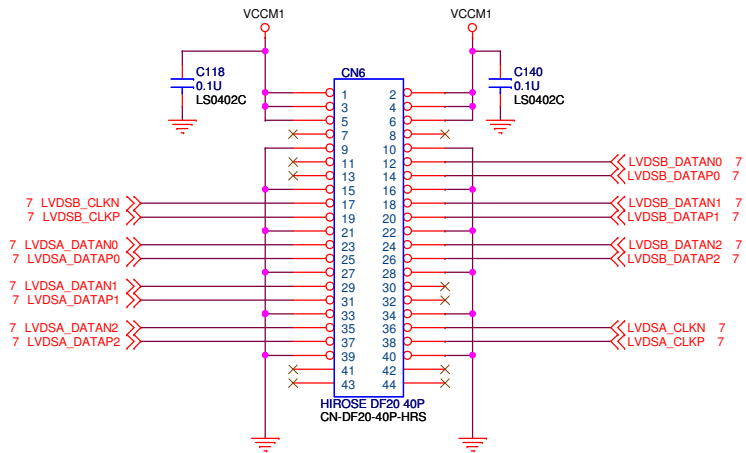
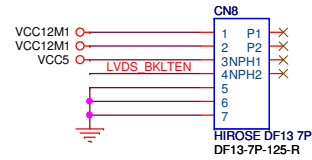
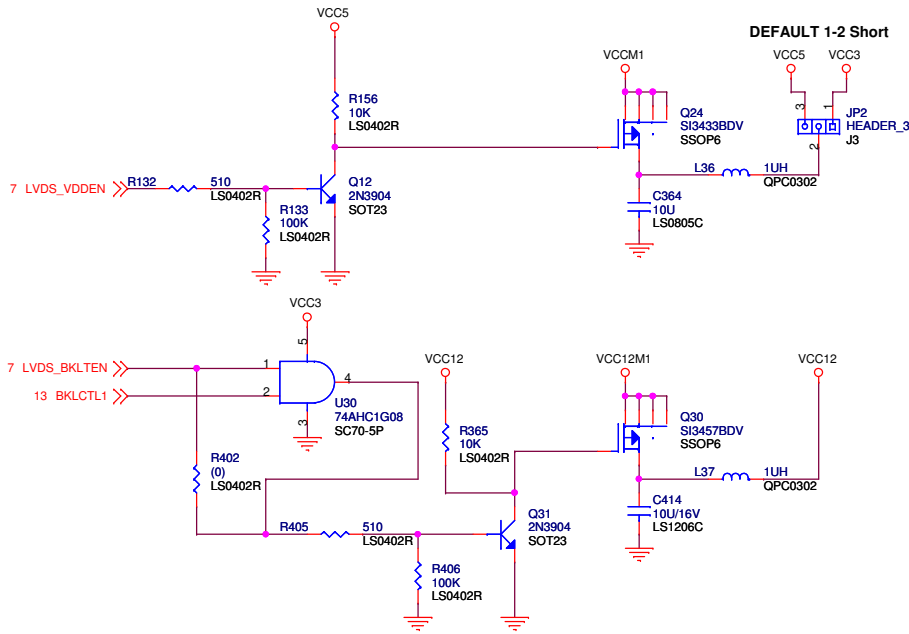
CF50P-3M-TP2-A

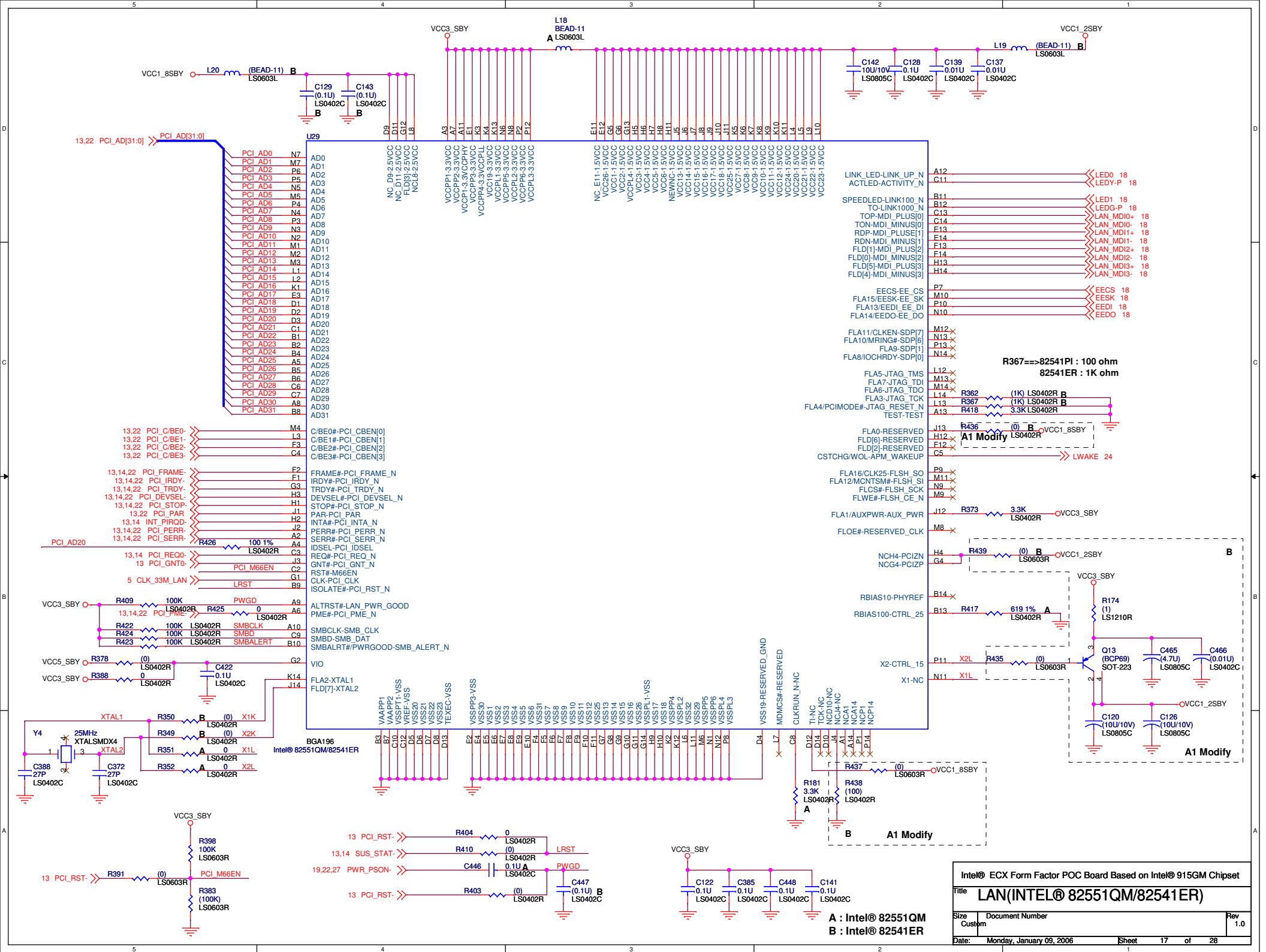


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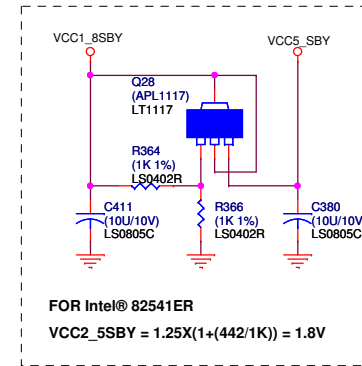
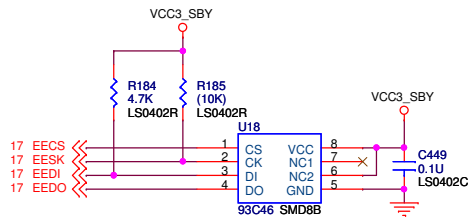
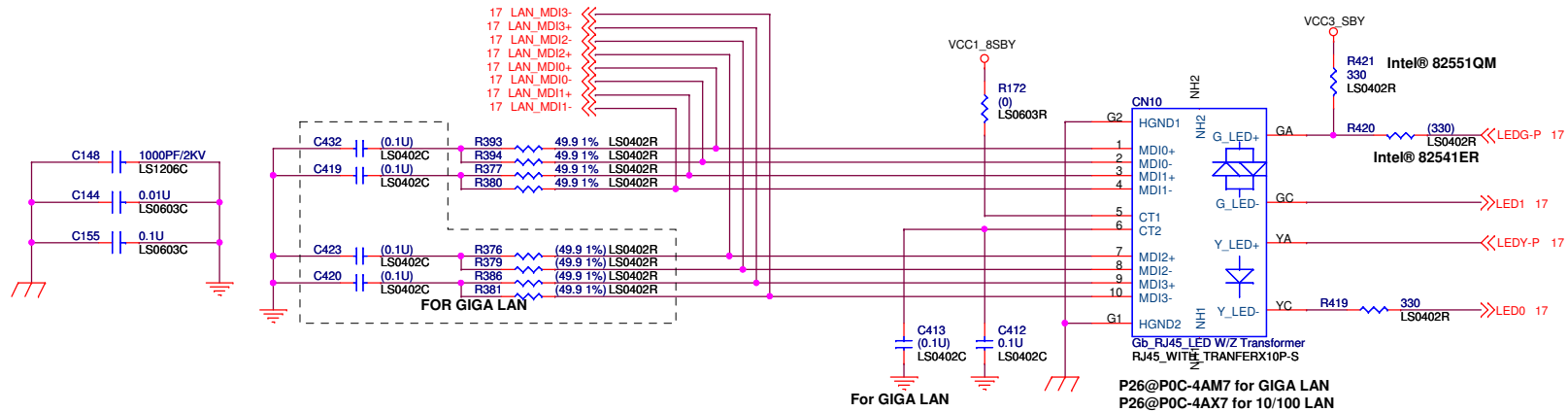


Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset			
Title		IDE,CF,USB,FP	
Size B	Document Number		Rev 1.0
Date:	Monday, January 09, 2006	Sheet	15 of 28

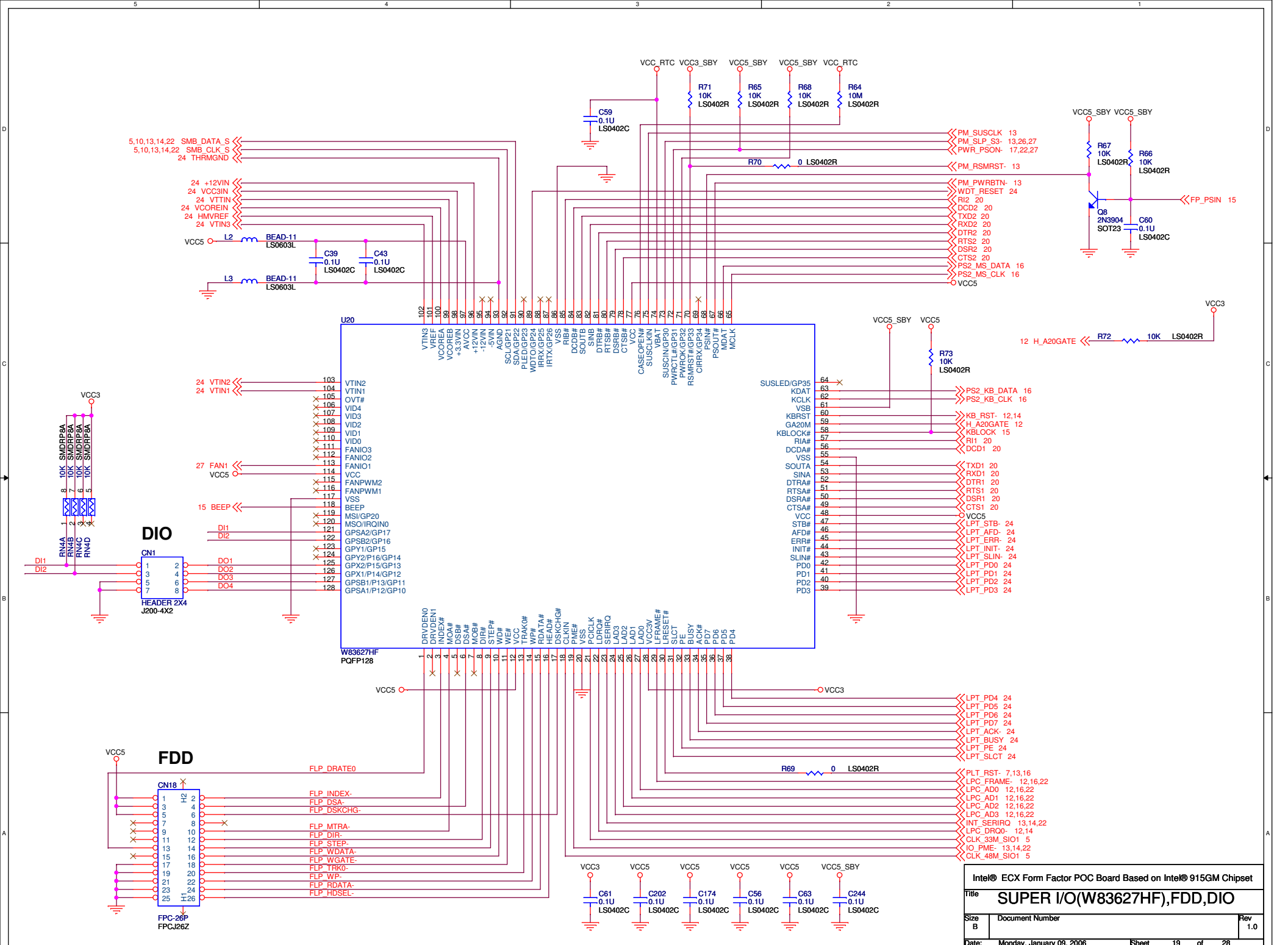




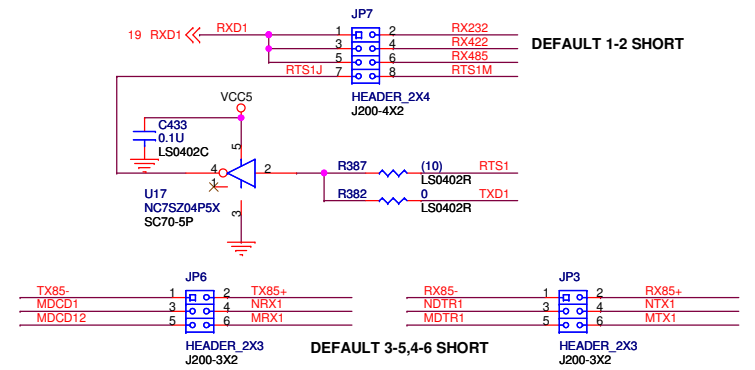
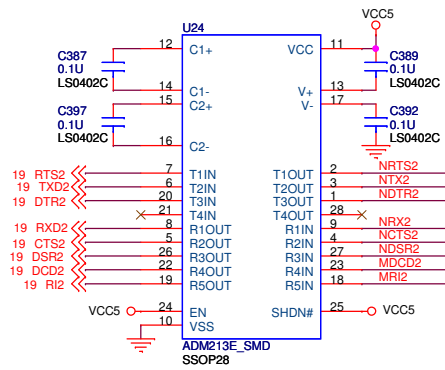
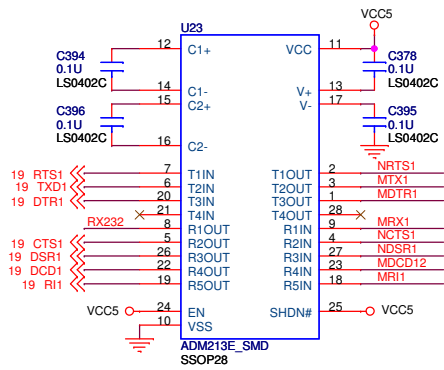
LAN RJ-45 --> 10/100MHz



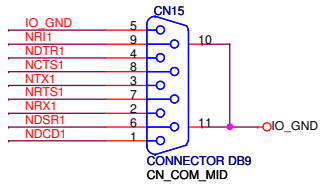
Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset		
Title		LAN RJ-45
Size B	Document Number	Rev 1.0
Date:	Monday, January 09, 2006	Sheet 18 of 28



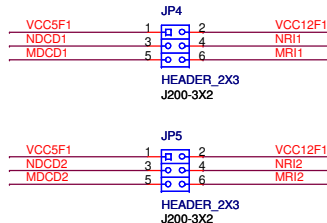
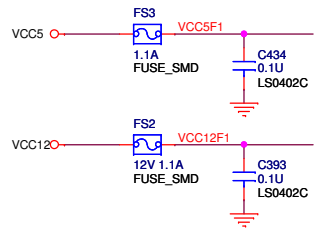
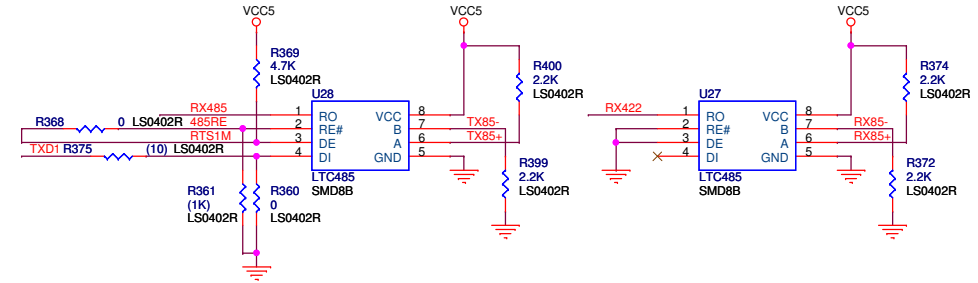
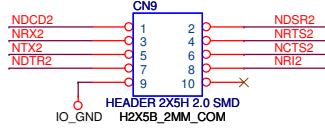
Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset			
Title			
SUPER I/O(W83627HF),FDD,DIO			
Size	Document Number		
	Rev 1.0		
Date:	Monday, January 09, 2006	Sheet	19 of 28



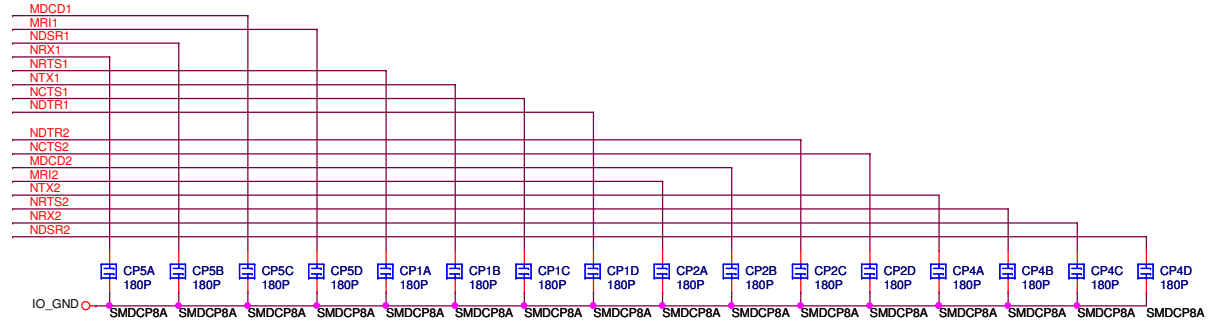
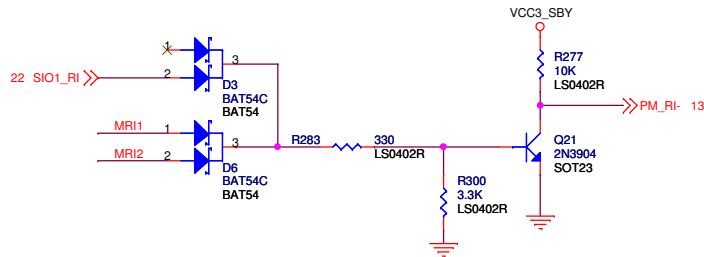
COM1



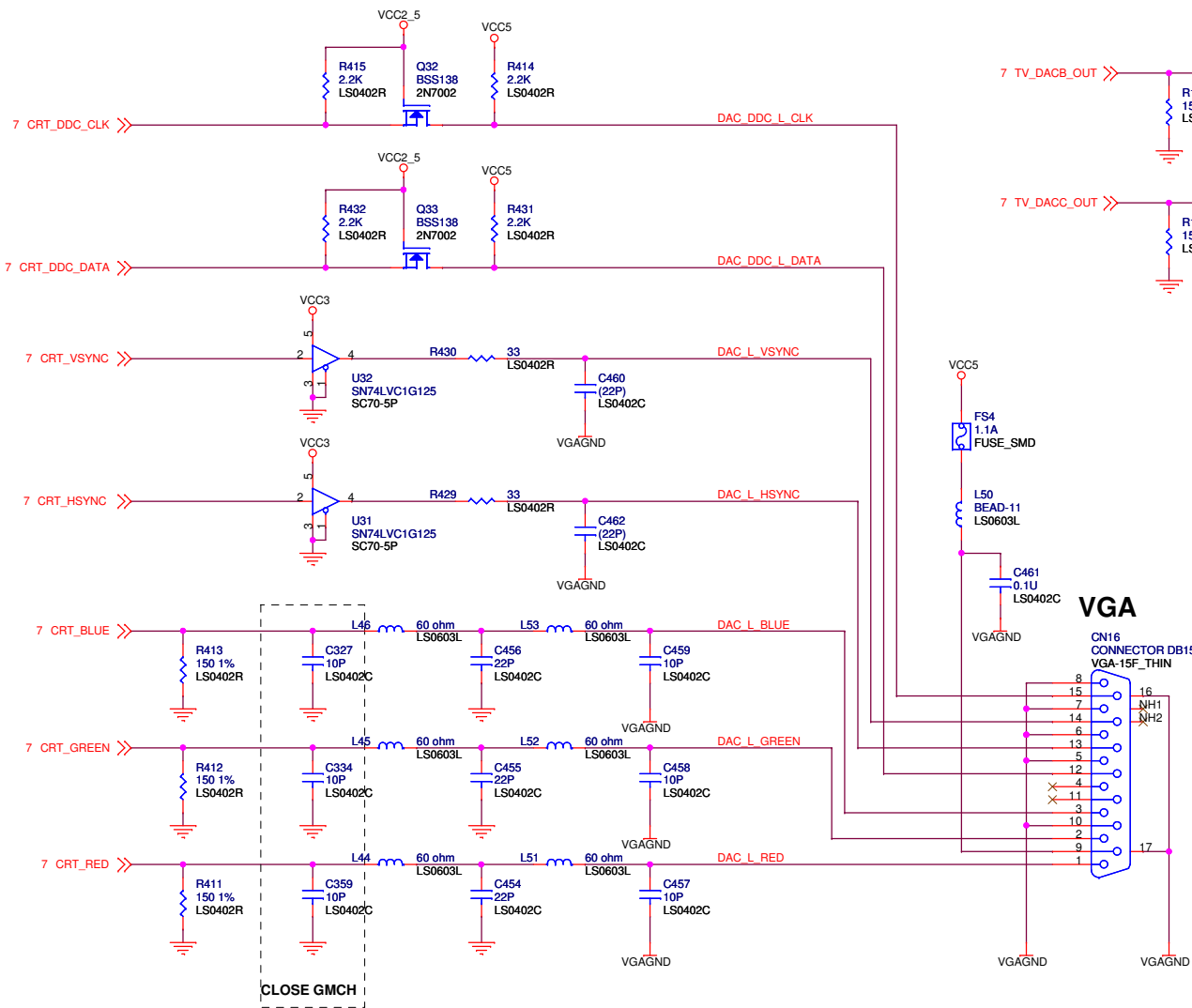
COM2



DEFAULT 3-5,4-6 SHORT



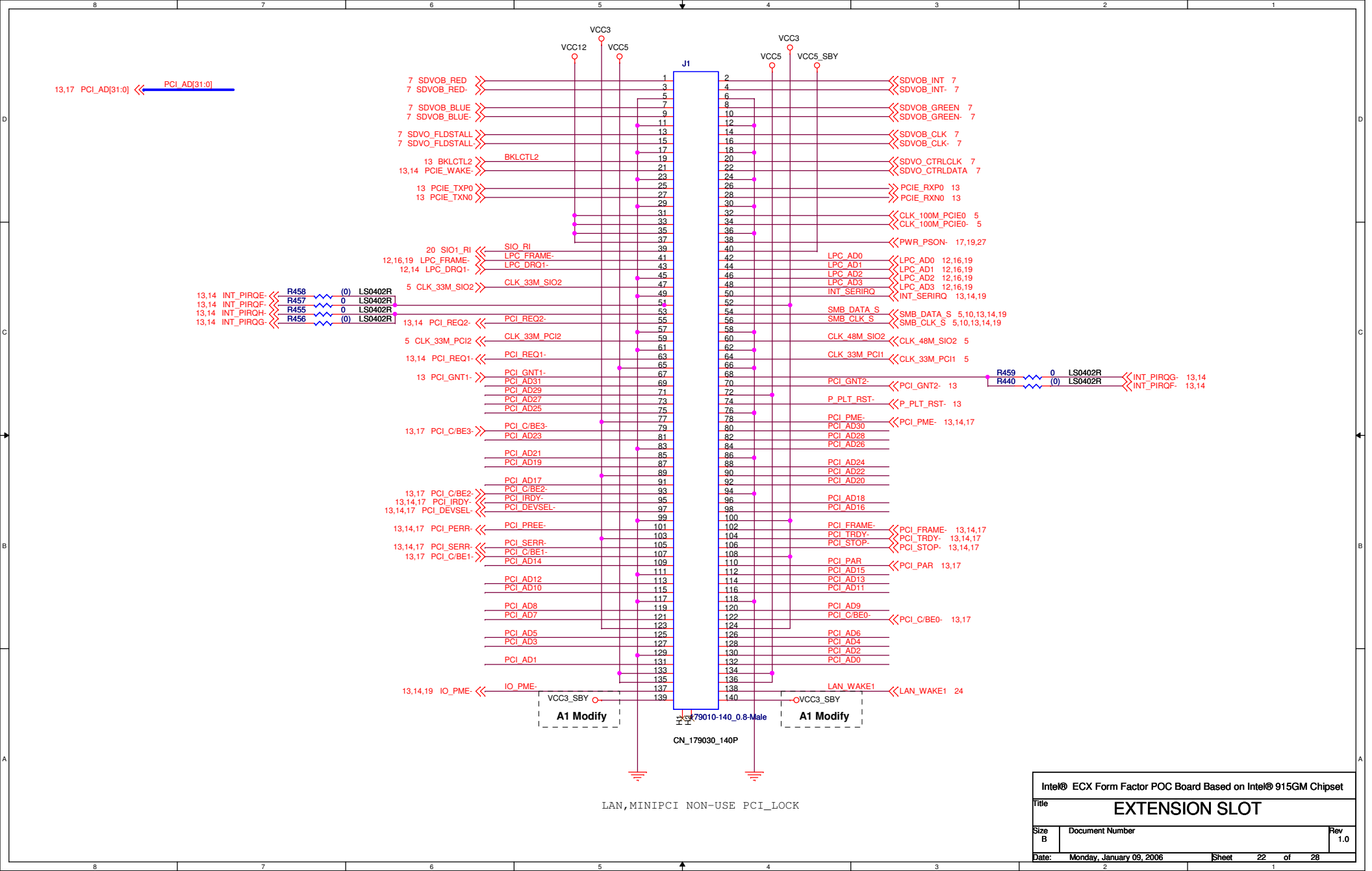
Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset		
Title		
COM PORT		
Size	Document Number	Rev
B		1.0
Date:	Monday, January 09, 2006	Sheet 20 of 26



VGA

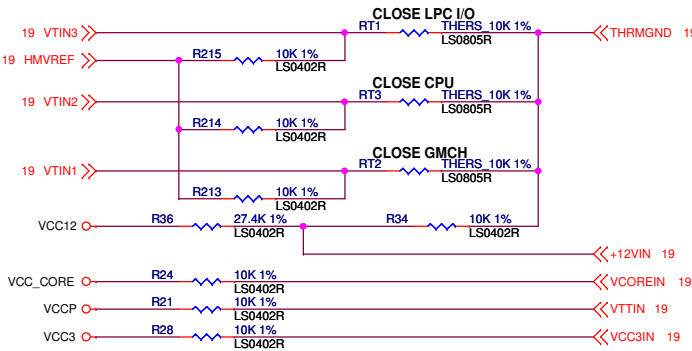
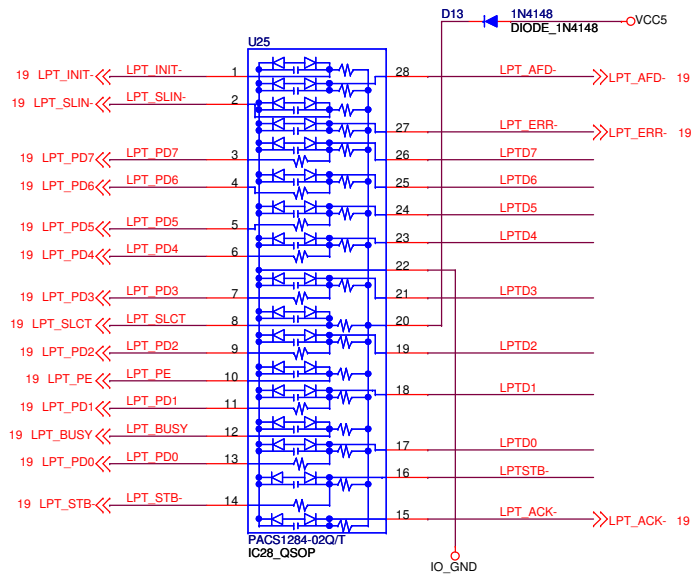
TV-OUT

Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset		
Title		
VGA,TV-OUT		
Size B	Document Number	Rev 1.0
Date:	Monday, January 09, 2006	Sheet 21 of 28

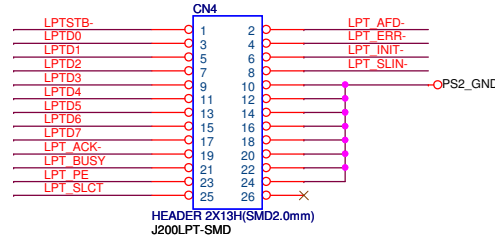


Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset		
Title		
EXTENSION SLOT		
Size	Document Number	Rev
B		1.0
Date:	Monday, January 09, 2006	Sheet 22 of 28

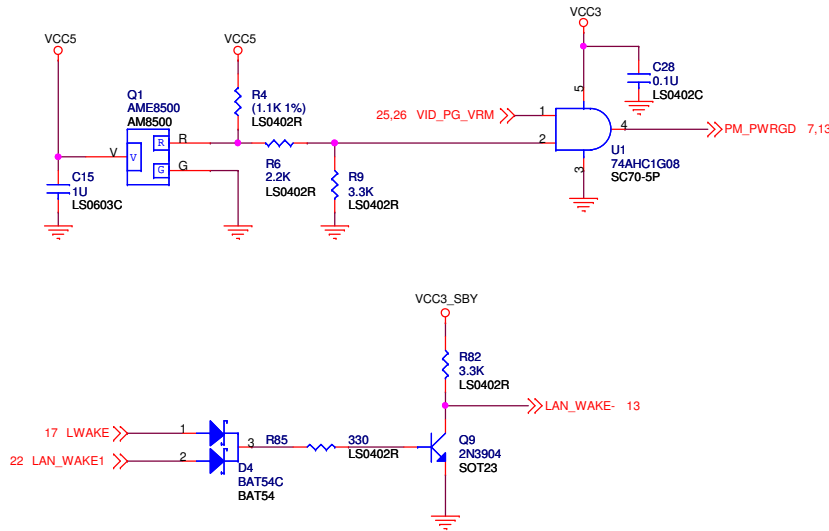
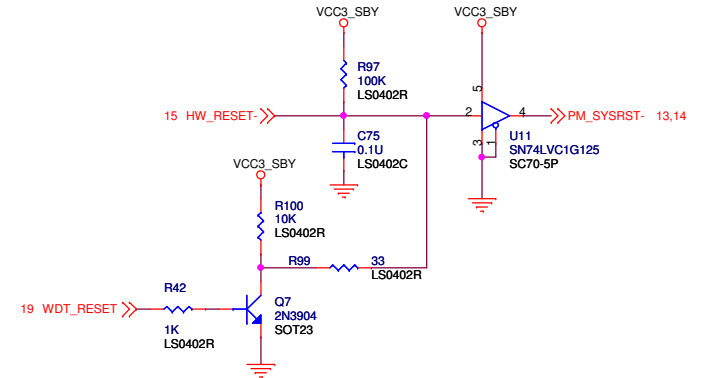
Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset		
Title AC-97 CODEC		
Size B	Document Number	Rev 1.0
Date: Monday, January 09, 2006	Sheet 23	of 28



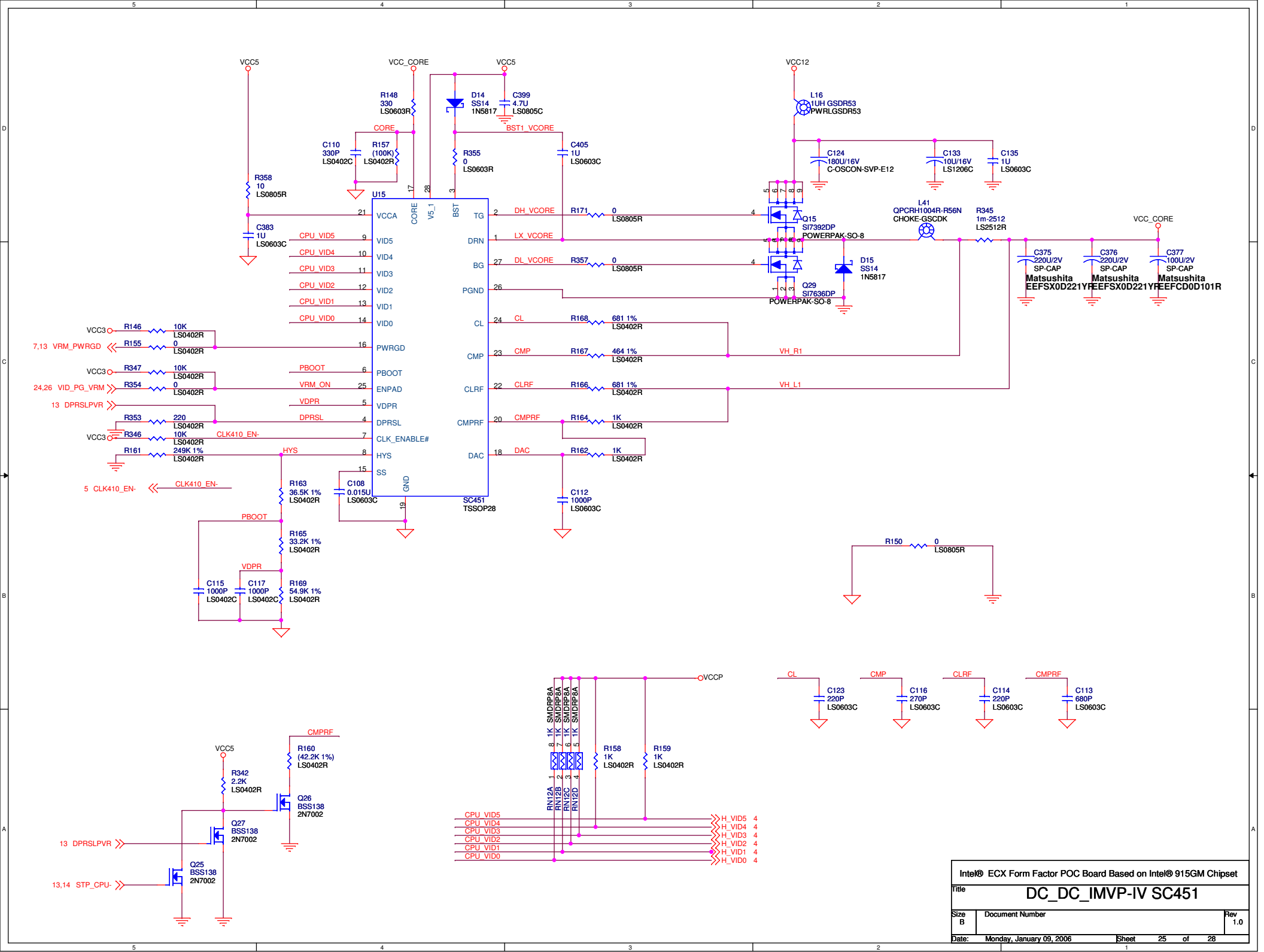
PARALLEL PORT

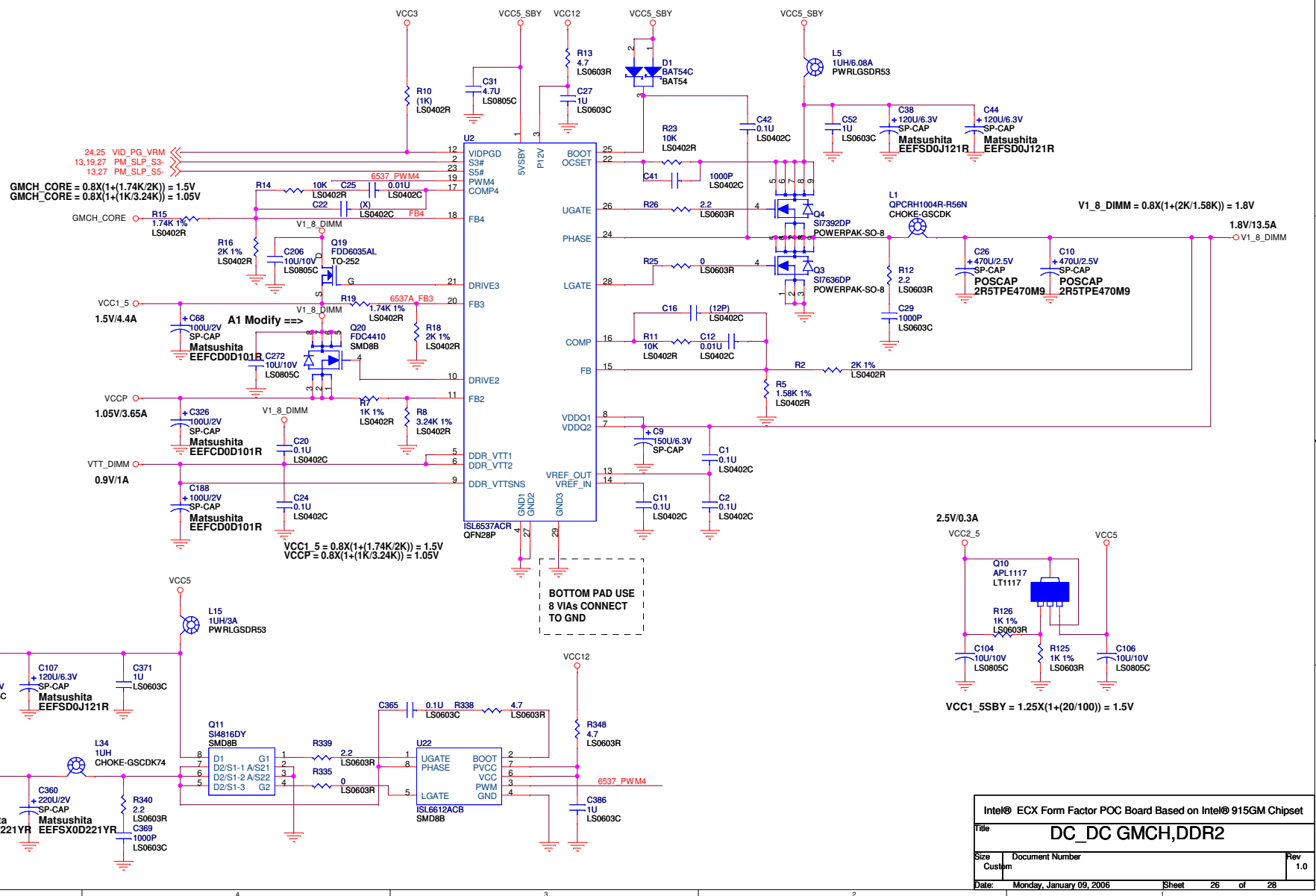


RESET

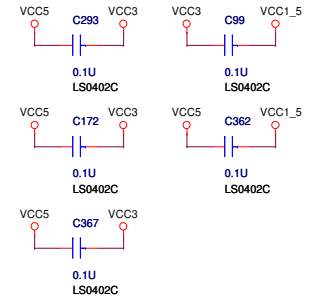
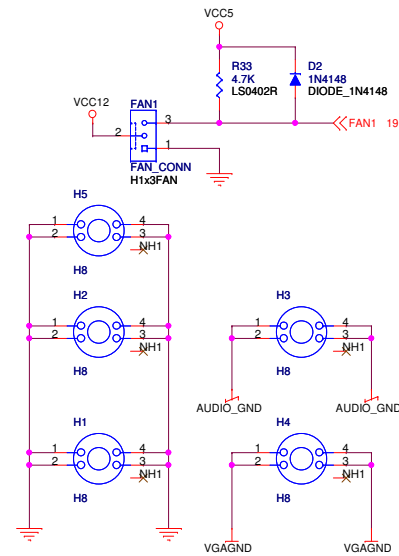
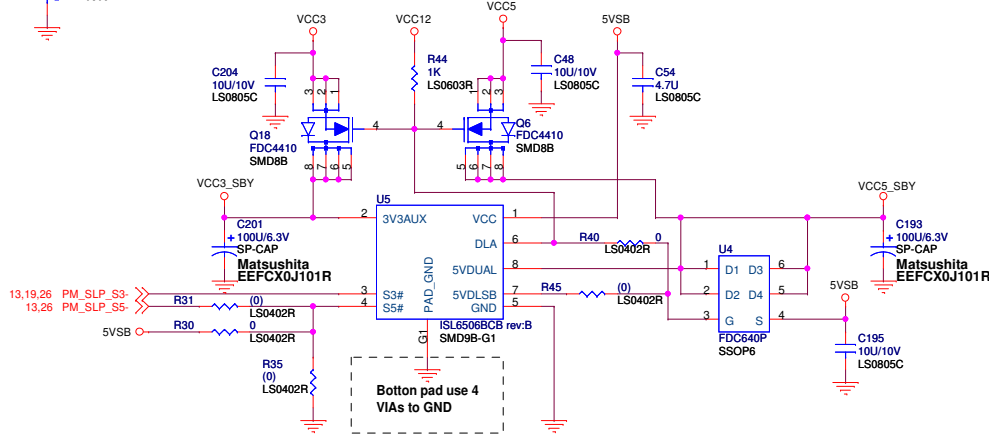
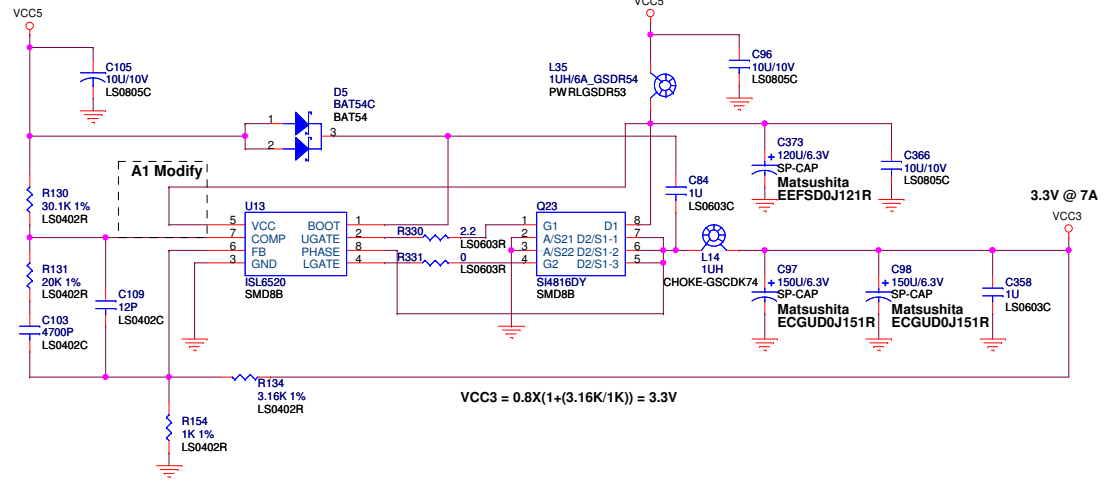
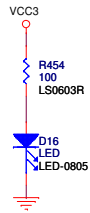
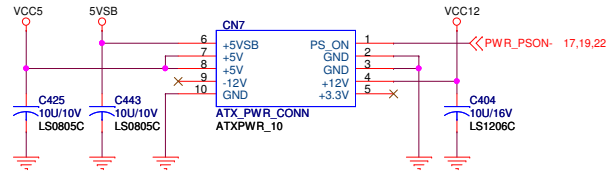


Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset			
Title		PRT,RESET,H/W MONITOR	
Size B	Document Number		Rev 1.0
Date:	Monday, January 09, 2006	Sheet	24 of 28





Power Connector



Intel® ECX Form Factor POC Board Based on Intel® 915GM Chipset			
Title		DC_POWER_IN	
Size	Custom	Document Number	Rev 1.0
Date:	Monday, January 09, 2006	Sheet	27 of 28

